

Form PTO-1449  
(Rev. 8-83)U.S. Department of Commerce  
Patent and Trademark Office

Atty Docket 0756-23

Serial No. 09/904,906

## INFORMATION DISCLOSURE STATEMENT

Applicants: Takeshi FUKADA et al.

Filing Date: July 16, 2001

Group Art Unit: 2825

## OTHER DOCUMENT

(Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner  
InitialInventor: Yasuhiko TAKEMURA, "Process for Fabricating Semiconductor Device" Filing Date: October 26, 2001,  
Specifications and Drawings for US Application Serial No. 09/983,882COPY OF PAPERS  
ORIGINALLY FILEDRECEIVED  
APR 24, 2002  
TECHNOLOGY CENTER 2800

Examiner

*J. M. Mabun*

Date Considered

1-12-03

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.